

Allegro[®] Platform System Requirements

**Product Version 16.3
December 2009**

© 1991–2009 Cadence Design Systems, Inc. All rights reserved.

Portions © Apache Software Foundation, Sun Microsystems, Free Software Foundation, Inc., Regents of the University of California, Massachusetts Institute of Technology, University of Florida. Used by permission. Printed in the United States of America.

Cadence Design Systems, Inc. (Cadence), 2655 Seely Ave., San Jose, CA 95134, USA.

Allegro PCB Editor contains technology licensed from, and copyrighted by: Apache Software Foundation, 1901 Munsey Drive Forest Hill, MD 21050, USA © 2000-2005, Apache Software Foundation. Sun Microsystems, 4150 Network Circle, Santa Clara, CA 95054 USA © 1994-2007, Sun Microsystems, Inc. Free Software Foundation, 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA © 1989, 1991, Free Software Foundation, Inc. Regents of the University of California, Sun Microsystems, Inc., Scriptics Corporation, © 2001, Regents of the University of California. Daniel Stenberg, © 1996 - 2006, Daniel Stenberg. UMFPACK © 2005, Timothy A. Davis, University of Florida, (davis@cise.ulf.edu). Ken Martin, Will Schroeder, Bill Lorensen © 1993-2002, Ken Martin, Will Schroeder, Bill Lorensen. Massachusetts Institute of Technology, 77 Massachusetts Avenue, Cambridge, Massachusetts, USA © 2003, the Board of Trustees of Massachusetts Institute of Technology. vtkQt, © 2000-2005, Matthias Koenig. All rights reserved.

Trademarks: Trademarks and service marks of Cadence Design Systems, Inc. contained in this document are attributed to Cadence with the appropriate symbol. For queries regarding Cadence's trademarks, contact the corporate legal department at the address shown above or call 800.862.4522.

Open SystemC, Open SystemC Initiative, OSCI, SystemC, and SystemC Initiative are trademarks or registered trademarks of Open SystemC Initiative, Inc. in the United States and other countries and are used with permission.

All other trademarks are the property of their respective holders.

Restricted Permission: This publication is protected by copyright law and international treaties and contains trade secrets and proprietary information owned by Cadence. Unauthorized reproduction or distribution of this publication, or any portion of it, may result in civil and criminal penalties. Except as specified in this permission statement, this publication may not be copied, reproduced, modified, published, uploaded, posted, transmitted, or distributed in any way, without prior written permission from Cadence. Unless otherwise agreed to by Cadence in writing, this statement grants Cadence customers permission to print one (1) hard copy of this publication subject to the following conditions:

1. The publication may be used only in accordance with a written agreement between Cadence and its customer.
2. The publication may not be modified in any way.
3. Any authorized copy of the publication or portion thereof must include all original copyright, trademark, and other proprietary notices and this permission statement.
4. The information contained in this document cannot be used in the development of like products or software, whether for internal or external use, and shall not be used for the benefit of any other party, whether or not for consideration.

Patents: Allegro PCB Editor, described in this document, is protected by U.S. Patents 5,481,695; 5,510,998; 5,550,748; 5,590,049; 5,625,565; 5,715,408; 6,516,447; 6,594,799; 6,851,094; 7,017,137; 7,143,341; 7,168,041; 7,464,358; 7,536,665; 7,562,330; 7,574,686.

Disclaimer: Information in this publication is subject to change without notice and does not represent a commitment on the part of Cadence. Except as may be explicitly set forth in such agreement, Cadence does not make, and expressly disclaims, any representations or warranties as to the completeness, accuracy or usefulness of the information contained in this document. Cadence does not warrant that use of such information will not infringe any third party rights, nor does Cadence assume any liability for damages or costs of any kind that may result from use of such information.

Restricted Rights: Use, duplication, or disclosure by the Government is subject to restrictions as set forth in FAR52.227-14 and DFAR252.227-7013 et seq. or its successor.

Contents

<u>Allegro® Platform System Requirements</u>	5
<u>Microsoft Windows System Requirements</u>	6
<u>Using Spaces in File and Directory Names</u>	7
<u>Sun Solaris System Requirements</u>	8
<u>Linux System Requirements</u>	10
<u>IBM AIX System Requirements</u>	11
<u>HP-UX System Requirements</u>	12
<u>Improving Performance on UNIX Systems</u>	13
<u>Displaying UI Dialog Boxes Correctly</u>	13
<u>Non-Native X Emulators</u>	13
<u>Graphics Requirements for Physical Design Products</u>	14
<u>Planning Hardware Purchases for Physical Design Products</u>	15
<u>Additional Recommendations for Allegro Package Designer and SiP Products</u>	16
<u>Compiler Requirements</u>	17
<u>Managing Licenses</u>	18

Allegro Platform System Requirements

Allegro[®] Platform System Requirements

This document contains the recommended system requirements for Cadence Silicon-Package-Board (SPB) tools, Release 16.3.

- [Microsoft Windows System Requirements](#) on page 6
- [Sun Solaris System Requirements](#) on page 8
- [Linux System Requirements](#) on page 10
- [IBM AIX System Requirements](#) on page 11
- [HP-UX System Requirements](#) on page 12
- [Improving Performance on UNIX Systems](#) on page 13
- [Displaying UI Dialog Boxes Correctly](#) on page 13
- [Non-Native X Emulators](#) on page 13
- [Graphics Requirements for Physical Design Products](#) on page 14
- [Planning Hardware Purchases for Physical Design Products](#) on page 15
- [Additional Recommendations for Allegro Package Designer and SiP Products](#) on page 16
- [Compiler Requirements](#) on page 17

Important

If you use a physical design product (Allegro PCB, APD, Allegro SI or Cadence SiP), be sure to read [Graphics Requirements for Physical Design Products](#) on page 14.

Note: All UNIX/Linux platforms: To ensure that your system is configured with the correct operating system patches, run the `checksysConf` program. You can download `checksysConf` from [Cadence Online Support](#).

Microsoft Windows System Requirements

This section describes the system requirements for Windows.

Because Cadence Silicon-Package-Board (SPB) products are integrated directly with Windows, hardware and peripherals supported by Windows are also supported by the Cadence SPB products. A list of hardware and peripherals officially supported by Windows can be obtained from the Microsoft web page.

Unlike previous releases, SPB products require updating certain Microsoft libraries in the Windows directory. You must install the Cadence software either using the desktop mode or client install. You may no longer be able to point to the Cadence software without installing.

Once you install 16.3, you should only use the 16.3 version of the switchversion program to change releases.

Platform ID wint

Operating System Windows 2008 Server, Windows 2003 Server, all Service Packs (32-bit only); Windows XP Professional; Windows Vista (32 and 64 bit) except Home Basic; 64 Bit Vista requires 64-bit Flex software dongle drivers (if using dongle licensing).

Hardware Intel IA-32 compatible (includes Intel P4 EMT and AMD Opteron™); 1.2 GHz minimum; 2.4 GHz or more is recommended.

Note: Cadence SPB products do not support the IPF chip.

CD-ROM drive

Ethernet card (for network communications and security hostID)

Three-button Microsoft-compatible mouse

For information about graphics cards, see [Graphics Requirements for Physical Design Products](#) on page 14.

Minimum	Recommended
1 GB physical memory	2 GB physical memory
10 GB disk space	50 GB disk space
virtual memory should be at least double the available physical memory	virtual memory should be at least double the available physical memory

Allegro Platform System Requirements

Allegro® Platform System Requirements

1024x768 resolution with
64,000 colors

1280x1024 color quality 32-bit
graphics

A dedicated graphics card is
recommended.

Note: Support for the Windows server does not include the Windows Remote Desktop (which is part of the terminal services package). No support for XP 64-bit.

Using Spaces in File and Directory Names

Support for spaces in file and directory names applies only to Windows. Spaces in file or directory names are not supported on UNIX platforms. Leading and trailing spaces in directory components are not supported.

Spaces in directory names are supported in the following areas:

- Program installation location (C:\Program Files)
- Default user home directory (C:\Document and Settings\<user>). If you set the HOME environment variable, you override the default.
- Default temporary directory (C:\Document and Settings\<user>). If you set the TEMP or TMP environment variable, you override the default.
- Your desktop directory (C:\Document and Settings\<user>\Desktop)
- Project location
- Library locations

Spaces in filenames are not supported when a filename is stored in the design. For example, symbols and padstack names are stored in the Allegro database where a space is not legal. Ancillary space support is based upon each SPB application. Allegro PCB Editor supports spaces in filenames for non-design files. Files that fall into this category are reports and text files.

Sun Solaris System Requirements

This section describes the system requirements for Solaris.

Platform ID	sun4v
Operating System	Solaris 10
Hardware	Sun UltraSparc or better. 1 GB (or greater) system memory 1 GB swap space 10 GB (or greater) available disk space TrueColor required For information about graphics cards, see Graphics Requirements for Physical Design Products on page 14.
Window Manager	Common Desktop Environment (CDE) or Gnome

Allegro Platform System Requirements

Allegro® Platform System Requirements

This section describes the system requirements for Solaris 86, supported June 2008.

Platform ID	sol86
Operating System	Solaris 10
Hardware	Sun Ultra Class or better workstations x86_64 (32 bit mode) CPU (x86_64 denotes 64 bit processors whose instruction sets are compatible with the X86 standard, such as Intel Xeon-EM64T, AMD Opteron, etc.) 1 GB (or greater) system memory 1 GB swap space 10 GB (or greater) available disk space TrueColor required For information about graphics cards, see Graphics Requirements for Physical Design Products on page 14. Note: Allegro PCB Editor will not be able to open 14.x and older databases. Use your original design platform to uprev older designs. Valor does not support sol86. PDF publisher is unavailable on this platform as Adobe does not provide the programming libraries for sol86 platform. Mentor translators are excluded as they require libraries from Mentor that are unavailable on sol86. Design Workbench is not part of 16.3.
Window Manager	Common Desktop Environment (CDE) or Gnome

Linux System Requirements

This section describes the system requirements for Linux.

Platform ID	Inx86
Operating System	RHEL 4.0 (32/64-bit) SP6; RHEL 5.0 (64-bit) and SLES9 (64-bit SP1 from Novell; kernel 2.6.5-7.139); SLES10 (64-bit) (SLES=SUSE)
Hardware	Intel P4 compatibles (includes AMD Opteron™), Intel P4 EMT and AMD Athlon™ 1 GB (or greater) system memory 2 GB swap space 10 GB (or greater) available disk space TrueColor required For information about graphics cards, see Graphics Requirements for Physical Design Products on page 14.
Window Manager	Gnome

Note: If you are running SPB back-end tools, you must source

```
<cdsroot>/tools/pcb/bin/cshrc (tcsh/csh)
```

or

```
<cdsroot>/tools/pcb/bin/profile (sh/bash)
```

or intergrate the equivalent Linux settings into your own environment files. It is not sufficient just to add SPB tools to the `PATH` variable.

IBM AIX System Requirements

This section describes the system requirements for IBM AIX.

Platform ID ibmrs

Operating System AIX 5.3

Hardware POWER3 and PC_604

Note: Cadence SPB no longer supports Power2 and older machines. Run the `lsattr -El proc0` command to check the CPU type; it should return a value containing the processor types listed above.

1 GB (or greater) system memory

2 GB swap space

10 GB (or greater) available disk space

TrueColor required

For information about graphics cards, see [Graphics Requirements for Physical Design Products](#) on page 14.

Window Manager Common Desktop Environment (CDE)

Backingstore needs to be enabled in the following manner:

1. Login as root.
2. Edit `/usr/lpp/X11/defaults/xserverrc`
3. Change the following line:
 EXTENSIONS = ""
 to read
 EXTENSIONS = "-bs"
4. Reboot your machine.

HP-UX System Requirements

SPB Release 16.3 does not operate on HP-UX platforms.

Improving Performance on UNIX Systems

You may be able to greatly enhance your graphics performance on certain machines if you run both X and Cadence SPB products on the same machine.

To run X and Cadence SPB products on the same machine, set the display variable to its local mode (type `setenv DISPLAY :0` at the command prompt). This lets the X protocol use shared memory instead of expensive TCP/IP transport.

Displaying UI Dialog Boxes Correctly

If the secondary (child) dialog boxes disappear behind the main UI of Allegro PCB Editor, you need to modify the window manager to keep child windows on top.

■ For Solaris

The typical window manager default configuration is

```
secondariesOnTop:False
```

- ❑ If you run CDE, add the following to your `~/.Xdefaults` file

```
DTwm*secondariesOnTop:True
```

- ❑ If you want to restrict this behavior to certain programs, add the following to your `~/.Xdefaults` file

```
DTwm*<program>*secondariesOnTop:True
```

For example:

```
DTwm*Allegro*secondariesOnTop:True
```

Add an entry to the file for each program. When finished, restart the window manager.

Non-Native X Emulators

Cadence tools only support the XServer provided by the OS platform vendors (Sun, Linux, and AIX). Non-native X solutions such as Hummingbird, Exceed, etc., are not supported. VNC to non-native X solutions is also not supported.

Graphics Requirements for Physical Design Products

Most physical design products (such as Allegro PCB Editor, APD, SiP, and SI, but not Allegro PCB Router or SigXplorer) offer enhanced graphics via OpenGL. Some Front-end programs require OpenGL for library viewing.

To use OpenGL as a graphics drawing option, your system must meet the following requirements:

- A modern computer purchased within the last couple of years.
- A dedicated graphics card (motherboard-based) with hardware OpenGL support or an Intel 945 class graphics card. A minimum of 128 MB dedicated (not shared) video RAM and a 128-bit bus interface (256 MB or more is recommended). We also recommend that the card be workstation certified. A high-end motherboard based graphics solution delivers acceptable performance for most designs. This is one area where spending a bit for quality improves productivity.
- A minimum of 1 GB system memory.
- Installation of the latest graphics patches from the graphics card vendor.

Important

As with most graphics support, you must ensure that the appropriate drivers are installed and properly configured on your system. If you use older versions, you may see glitches with the display of objects, poor performance, and other problems. In the case of Windows Vista, only DirectX is available from the initial installation, so you must obtain new drivers before you attempt to run SPB tools. Make sure that video cards for Linux have Linux drivers available.

Remote graphics are not supported. Examples include:

- Windows terminal services such as Citrix
- VPN based programs
- Remote X programs (for example: Hummingbird)
- Thin client solutions (for example: SunRays)
- Remote X clients (for example: Sun to Sun)

As of Release 16.0, all SPB tools require at least 65000 colors. We no longer support 256-color mode (also known as 8-plane mode in the X window world). Unix/Linux Xservers must be configured to use the TrueColor model.

Allegro Platform System Requirements

Allegro® Platform System Requirements

Only the 2D mode is supported. OpenGL requires higher level graphics cards for best performance. On Solaris and AIX platforms, OpenGL requires TrueColor 24 bit graphic settings, and will not display all colors if the system defaults are 8 bit color.

OpenGL is enabled by default. You can disable it using the environment variable `disable_opengl` in the OpenGL category of the User Preferences Editor dialog box.

Planning Hardware Purchases for Physical Design Products

The SPB product family includes products such as schematic capture and library design. These place higher demands on disk access and do not tend to need the fastest CPU available. However, most Allegro back-end products are CPU and memory-bound—especially true of the back-end products: Allegro PCB Editor, PCB Router, APD, and PCB SI. Therefore, Cadence recommends a faster CPU for these products.

Allegro products use both integer and floating point, so select a configuration that provides ample processing power in both areas. When choosing a machine, purchase one with the highest CPU rating. Because vendors are de-emphasizing their CPU clocking, use the vendors' chip-naming convention. Alternatively, use a performance benchmark measurement. For example, the SPEC site (<http://www.spec.org>) lists the hardware results from multiple vendors.

If two systems have comparable ratings, purchase the system with the larger Level 2 cache, even if its ratings are slightly slower. Buying a top-end CPU usually also brings a system with the latest motherboard, bus architecture, and RAM hardware.

In the Windows environment, if the machine is recommended for gamers, it meets the needs of high-end physical implementation design. The exception to this rule is that for Allegro products, you do not need dedicated sound cards. A dedicated graphics card is recommended to a motherboard-based graphics card because motherboard cards share memory and bus access with the CPU.

Buy enough memory so you are not paging during your work. One gigabyte is a good starting point for average PCB designs but you may need to raise the total if you plan on auto-routing, signal integrity work, or multi-board simulation. A rule of thumb is to take a recently completed board, and your memory requirement would be:

`Memory requirements = 1000 Megs + (Design_Size_on_disk * 10)`

then round up to the next half gigabyte.

Allegro Platform System Requirements

Allegro® Platform System Requirements

Example: If you have a 50 MB board, then you would need 2 Gbyte of memory.

If you plan on using centralized Cadence software, design, or library storage, a 100 Mbs network connection is recommended.

While SPB products do not require 64-bit CPUs, our tools perform better on the 64-bit architectures. Note that all modern CPUs are now 64-bit capable. The exception is that we do not support the Intel Itanium chip.

While SPB products do not take advantage of multi-processors, having a second processor can be advantageous in both the Windows and UNIX (Linux) environments.

- On Windows, the second chip can remove the performance penalty that is imposed by Virus checkers, inventory management, and other overhead software that can be found installed on modern Windows systems. In this area, the Intel HT technology can help with Windows “overhead” processing.
- On UNIX systems, graphics programs will achieve better performance due to the nature of the X-windows architecture. The additional CPUs also will allow you to run background processes, such as auto-routers and simulators.

In the Intel CPU world, Intel, Xeons and AMD chips typically leapfrog each other on which is the top performer. You will have better performance using a 64-bit-capable chip over a 32-bit-only CPU (excluding the Intel IPF chip).

If you are considering a laptop computer, look at the “workstation replacement” laptops, even though they are heavier and have less battery life than more conventional laptops.

Finally, when purchasing a new system, look at your future needs and not your current requirements.

Additional Recommendations for Allegro Package Designer and SiP Products

The Cadence 3D Design Viewer (standard in SiP, optional add-on for APD) requires an OpenGL-compliant video card (128 MB recommended minimum video memory).

As of Release 16.0, IC-Package co-design capability (available in Package Designer XL and SiP) is available only on the Solaris and Linux platforms. Likewise, since this capability works with the Encounter-based IC floor-planning technology, you should plan that systems running this capability have sufficient disk and memory space for the Encounter-based and Allegro portions of the applications, as well as sufficient disk space for the IC portions of your system designs.

Compiler Requirements

Microsoft Windows

Compiler:	Visual Studio.Net 2005
Required Compiler	32 bit compiler
Options:	8 byte structure alignment (<code>-Zp8</code>) multi-thread dll (<code>-MD</code>) cdecl calling convention (<code>-Gd</code>)

Sun Solaris

Compiler:	Studio 12
Required Compiler	32 bit compiler
Options:	8 byte structure alignment (<code>-dalign</code>) multi-threaded (<code>-D_REENTRANT</code>) Position independent code (<code>-KPIC</code>)
Required DLL Linker Options:	multi-threaded (<code>-mt</code>)

Sun Sol86

Compiler:	Sun Studio 12
Required Compiler	32 bit compiler
Options:	8 byte structure alignment (<code>-dalign</code>) multi-threaded (<code>-D_REENTRANT</code>) Position independent code (<code>-KPIC</code>)
Required DLL Linker Options:	multi-threaded (<code>-mt</code>)

Linux

Compiler:	gcc/g++ 4.1.2
-----------	---------------

Allegro Platform System Requirements

Allegro® Platform System Requirements

Required Compiler 32 bit compiler
Options: position independent code (`-m32 -fPIC`)

Required DLL Linker `-fPIC -shared`
Options:

IBM AIX

Compiler: VA 7.0

Required Compiler 32 bit compiler
Options: language level support (`-qlanglvl=extended:redefmac`)
 C++ name mangling (`-qnamemangling=v5`)

Required DLL Linker `-brtl -G`
Options:

Managing Licenses

All SPB tools support the use of an `options` file, which you can use to restrict user access and manage licensing beyond the limits of the license file. To have products return their licenses to the license pool when they are idle, SPB tools let you add a `TIMEOUT` line, which sets a maximum amount of time (in seconds) that a license can remain inactive, to the `options` file. The queuing argument of the `NOLOG` line in the options file, however, is not supported.

For more information about licensing and the `options` file, refer to the Cadence License Manager document.